

## REMARKS

Claims 1-14, 16-38, 40-62, and 64-102 are now pending in the application. Claims 15, 39, and 63 are cancelled without disclaimer or prejudice to the subject matter contained therein. Support for the amendments to the claims contained herein can be found throughout the drawings and specification. As such, no new matter is added. The Examiner is respectfully requested to reconsider and withdraw the rejections in view of the amendments and remarks contained herein.

## REJECTION UNDER 35 U.S.C. § 102

Claims 1-4, 6, 7, 9, 10, 12-17, 20, 22, 23, 25-28, 31, 33-34, 36-41, 44, 46, 47, 49-52, 55, 57, 58, 60-65, 67, 68, 70, 71, 73-102 are rejected under 35 U.S.C. § 102(e) as being anticipated by Bear et al. (U.S. Pub. No. 2004/0225901). This rejection is respectfully traversed.

With respect to claim 1, Bear fails to at least show, teach, or suggest a secondary processor that dissipates less power than the primary processor. As best understood by Applicant, Bear appears to be silent as to the relative power dissipation of the alleged primary and secondary processors.

For anticipation to be present under 35 U.S.C §102(b), there must be **no difference** between the claimed invention and the reference disclosure as viewed by one skilled in the field of the invention. *Scripps Clinic & Res. Found. V. Genentech, Inc.*, 18 USPQ.2d 1001 (Fed. Cir. 1991). All of the limitations of the claim must be inherent or expressly disclosed and must be arranged as in the claim. *Constant v. Advanced Micro-Devices, Inc.*, 7 USPQ.2d 1057 (Fed. Cir. 1988). Here, Bear fails to

disclose the limitation of a secondary processor that dissipates less power than a primary processor.

For example, the Examiner relies on FIGS. 1 and 2 of Bear to disclose an auxiliary processor 12 (i.e. as the alleged secondary processor). An auxiliary computing device 10 as shown in FIGS. 1 and 2 includes the auxiliary processor 12. The Examiner presumably relies on processing unit 21 (located in computing device 20) to disclose the primary processor. Applicant respectfully notes that the Examiner makes no specific allegation that the alleged secondary processor dissipates less power than the primary processor. Applicant further notes that Bear appears to lack any disclosure relating to relative power dissipation of the auxiliary processor 12 and the processing unit 21.

Applicant respectfully notes that Paragraphs [0032]-[0039] in particular describe the auxiliary processor 12 and the processing unit 21 and appear to be absent of any teaching or suggestion that the auxiliary processor 12 dissipates less power than the processing unit 21. Similarly, FIG. 4 and corresponding description Paragraphs [0043]-[0052] discuss power state transitions of the processors but fail to disclose relative power dissipation of the processors.

Applicant respectfully submits that claim 1, as well as its dependent claims, should be allowable for at least the above reasons. Claims 12, 25, 36, 49, and 60, as well as their corresponding dependent claims, should be allowable for at least similar reasons.

CONCLUSION

It is believed that all of the stated grounds of rejection have been properly traversed, accommodated, or rendered moot. Applicant therefore respectfully requests that the Examiner reconsider and withdraw all presently outstanding rejections. It is believed that a full and complete response has been made to the outstanding Office Action and the present application is in condition for allowance. Thus, prompt and favorable consideration of this amendment is respectfully requested. If the Examiner believes that personal communication will expedite prosecution of this application, the Examiner is invited to telephone the undersigned at (248) 641-1600.

Respectfully submitted,

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